**Verification Futures Conference UK (01 July 2025) – Webinar Joining Instructions**

Hello,

We are looking forward to you joining us on Tuesday for the Verification Futures Conference!!  The full agenda and speaker information are available on the [Tessolve website](https://www.tessolve.com/verification-futures/vf2025-uk/).

This year we have above following tracks/sessions:

* **Main Morning AM**
* **Verification Projects using Open Source/License-Free Tools**
* **Training Engineers in work**
* **AI Design IP**
* **FPGA AM**
* **Main Afternoon PM**
* **Mixed Signal**
* **Design**
* **FPGA PM**
* **CPU/RISCV Verification**
* **AI in DV**
* **Breakthrough Technologies**
* **FPGA Training**

We recommend you take a few moments before Tuesday to register for the Webinar, if you wish to attend by using the links below.

(**This year the conference will be delivered by a Microsoft Teams webinar**). After registration, you will receive a confirmation email containing the unique link for your Webinar(s).

P.S: The links allow 15 minutes for session preparation, so a session starting, for example, at 11:00 am, will be available from 10:45 am.

* **Conference Program**

[**Registration Link**](https://events.teams.microsoft.com/event/c677367f-b709-4539-baad-d5ed3e94e899%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

08:30 - Exhibition open and student posters available for viewing

09:25 - Conference starts

09:30 - Conference Keynote - Sean Redmond (Silicon Catalyst UK)

10:00 – Dr Andy Penrose (Cadence)

**10:30 - Break**

* [**Main Morning AM – Registration Link**](https://events.teams.microsoft.com/event/c677367f-b709-4539-baad-d5ed3e94e899%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

11:00 - Larry Lapides, (Synopsys)

11:30 - Johannes Müller, (RPTU Kaiserslautern-Landau)

11:50 - Surinder Sood, (ARM)

12:10 - Matt Bridle, (Doulos)

* [**Verification Projects using Open Source/License-Free Tools – Registration Link**](https://events.teams.microsoft.com/event/a7a091fe-8fe3-49b7-aa0e-b80fc52dba68%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

11:00 - Andy Bond (Axelera AI)

11:30 - Brodie Grant (Platform Recruitment)

11:50 - Abdelrahman (Si-vision)

12:10 - Christian Tchilikov (Semify GmbH)

* [**Training Engineers in work – Registration Link**](https://events.teams.microsoft.com/event/d20dc203-3b7b-4623-ad0f-52d90aa883f5%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

11.00 - Matthew Taylor (Doulos)

* [**AI Design IP – Registration Link**](https://events.teams.microsoft.com/event/40f97588-228a-4881-9b25-4953a1be94ba%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

11:00 - Brice Gaignoux (Cadence/Secure IC)

11:30 - Martin Zeller (Dreamchip)

11:50 - Gareth Richard (TechWorks –AI)

12:10 - James Lewis (Red Semiconductor)

* [**FPGA AM – Registration Link**](https://events.teams.microsoft.com/event/bfd5fd68-f328-459e-8410-4ae56b088ec4%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

11:00 - Ian Pearson (Microchip)

11:30 - Steinn Gustafsson (Chevin Technology)

11:50 - Owen Bateman (Quick Logic)

12:10 - Dr. Pedro Machado (NTU)

**12:30 - 13:30**  **Lunch**

* **[Main Afternoon PM – Registration Link](https://events.teams.microsoft.com/event/c677367f-b709-4539-baad-d5ed3e94e899%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)**

13:30 - Yassine Eben Aimine (Siemens)

14:00 - Tobias Ludwig (LUBIS EDA)

14:20 - David Kelf (Breker )

14:40 - Neil Dickens (IC-Resources)

15:30 - Antoine Madec (Axelera AI)

15:50 - Will Keen (Fractile Ltd)

16:10 - Surinder Sood (Arm)

* [**Mixed Signal – Registration Link**](https://events.teams.microsoft.com/event/86b97e43-1576-4b90-8160-9bc416ebeaff%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

13:30- Daniel Cross, Tim Pylant (Cadence)

14:20 - Peter Grove (Renesas)

14:40 - Paul Denny (pSemi)

15:30- Evgeny Vlasov (Synopsys In)

16:10 - Steve Holloway (Renesas)

* [**Design – Registration Link**](https://events.teams.microsoft.com/event/7ad68899-6d7c-4f04-9054-8ef424e97463%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

13:30- Priyankari Chevuturi   (Synopsys)

14:00 – John Wickerson, Imperial College London

14:20 - Doug Carson (Keysight Technologies)

14:40 – Georg Meinhard (Difflogic)

* [**FPGA PM – Registration Link**](https://events.teams.microsoft.com/event/11938083-8eef-4acd-9ea4-b5bde4cefe94%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

13:30 - Michael O’Sullivan (Cadence)

14:00 - Alexander Montgomerie-Corcoran (Heronic Technologies)

14:20- Marius Elvegard, (Inventas)

14:40- Yassine Eben Aimine (Siemens)

**15:00 – Refreshments Break**

* [**CPU/RISCV Verification – Registration Link**](https://events.teams.microsoft.com/event/fe0deff2-8ac7-489f-a9f0-3c7625fd97de%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

15:30 - Valentin Peltier (Cadence -Secure IC)

15:50- Christian Appold (DENSO AUTOMOTIVE Deutschland GmbH)

16:10- Katharina Ceesay-Seitz (ETH Zurich)

* [**AI in DV – Registration Link**](https://events.teams.microsoft.com/event/29184718-79c3-4244-b3bb-5ecee0ece7e9%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

15:30 – Marmik Soni (Tessolve)

15:50- Arjumand Yaqoob (Qualcomm Incorporated)

16:10- Vidushi Yaksh & William Ly (University of Southampton)

* [**Breakthrough Technologies – Registration Link**](https://events.teams.microsoft.com/event/9a888cee-aa58-4182-b6a7-3b4dee36a7eb%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

15:30 – Mark Knight (Arm)

15:50- Cristian Sestito (University of Edinburgh)

16:10- Kauser Johar - (Chipletti)

* [**FPGA Training – Registration Link**](https://events.teams.microsoft.com/event/a2bda1e7-55c6-4fa2-8d9b-ce7a9ea97e5b%40c4d9f3a1-5848-44dc-b9a7-5acb8f1c18c4)

15:30 - Matt Bridle (Doulos)

**16:30 – Drinks and Pizza, student poster competition prize giving, sponsor gift raffle draw**

**17:00 – End of conference**

**You will be able to exit and re-join the Main Conference using your original link between tracks if you leave to attend the breakout sessions**

We hope you find the information helpful, but should you have a question please don’t hesitate to contact (**VFutures**) who will be pleased to help.

**Best Regards,**

**Tessolve Conference Team**