

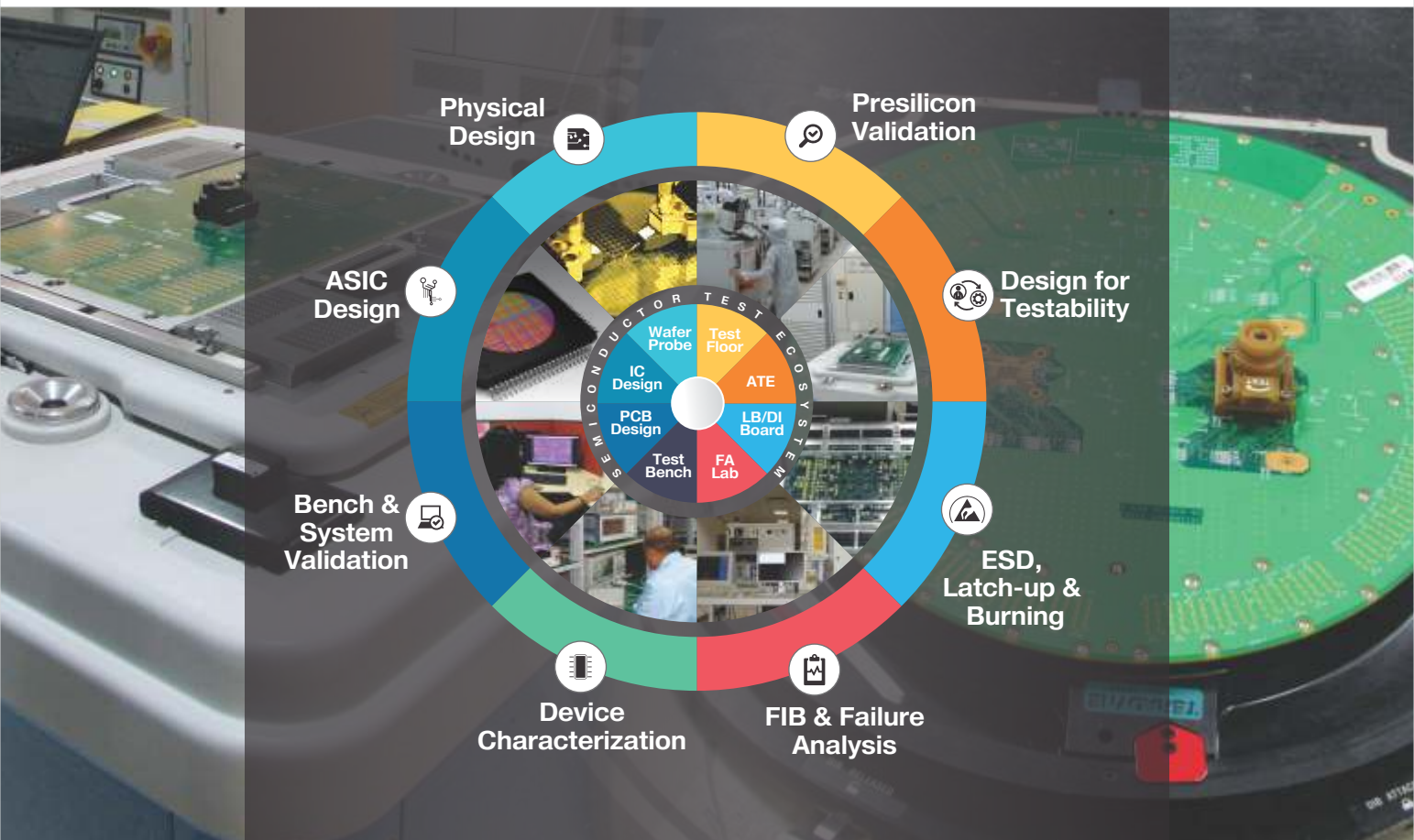
# IC DESIGN AND TESTING

**Tessolve a leading solutions provider, targeted specifically to help our semiconductor customers in the various phases of their product life cycle.**

We provide IC Design, Pre-Silicon Validation and Post-Silicon Test and Product engineering Solutions and Services for today's high-speed digital, RF, Analog, PMIC, Mixed-signal, MCU/MPU, MEMS, Image Sensors, Memory to complex devices like ASIC/ASSP, SoC / SiP devices and emerging technologies in the 2.5/3D space that go into automotive, aerospace, IoT wearable, wireless, computing, network / fiber optics and other consumer electronics.



Partner with us for high quality IC design closure, cost effective and most comprehensive Pre-silicon and Post silicon Solutions and Services.

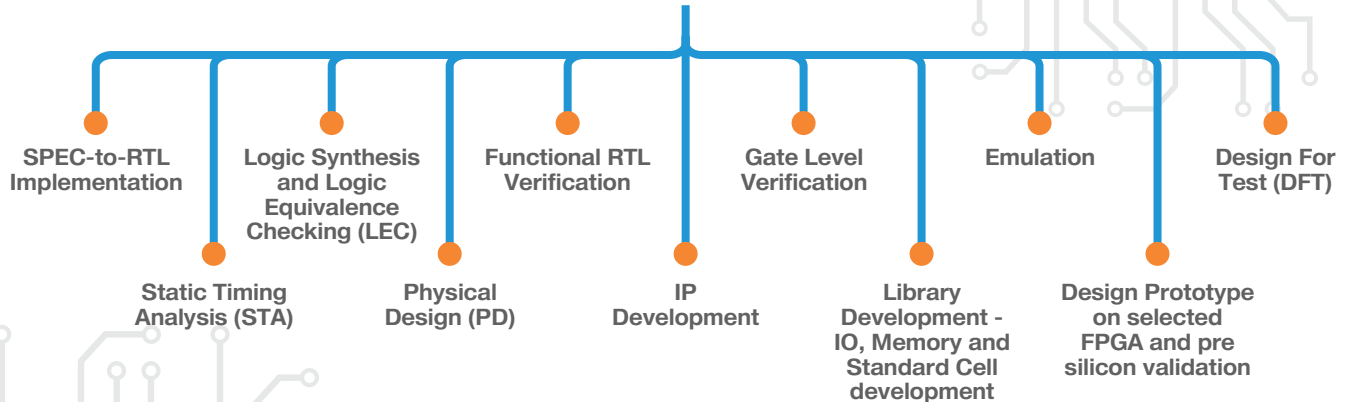


## Tessolve Solutions and Services

**Pre Silicon validation ensures device design correctness and Design For Testability (DFT) eases post silicon complex device testing.**



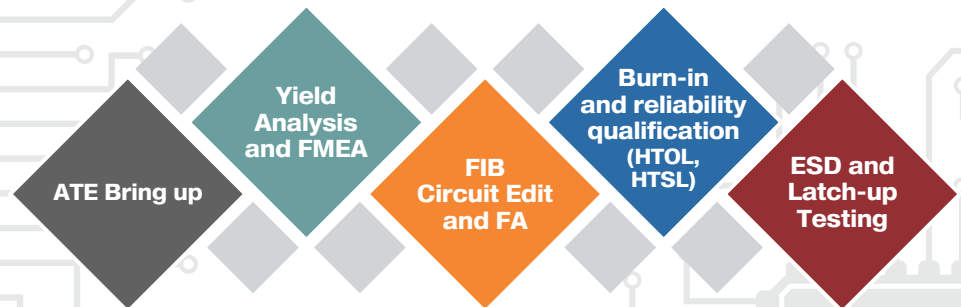
### IC Design & Pre Silicon



**ATE bring up and device characterization ensures device quality and meets time to market race.**



### Post Silicon ATE Bring up



- Test Plan and Program Development
- Test Program & Device Debug (Wafer and Package)
- ATE Test Fixture and Interconnect Design
- Test Time Optimization
- Device PVT Characterization on ATE
- Device Bench characterization
- Conversion – Multisite, Platform, Vector

Reliability qualification demonstrates the fitness of your IC for use in the field and helps our customers better understand the wear-out mechanisms.



Delivering Excellence in Semiconductor Engineering

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